EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S1	5	"6388989"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 08:23
S2	2	"6388989".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 09:00
S3	160	719/312.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 17:00
S4	23	S3 and ((first and second) near3 (processor microprocessor DSP controller microcontroller))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 09:09
S5	440	((first and second) near3 (processor microprocessor DSP controller microcontroller)) with (share$4 near3 (memory buffer))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 09:10
S6	125	S5 and (allocat$4 assign$4 designat$4 determin$4) with (buffer memory) with (request command)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 09:12
S7	115	S6 and ((buffer memory) near3 (pointer descriptor handle location address))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 09:47
S8	21	S5 and (first near3 (processor microprocessor DSP controller microcontroller)) with (allocat$4) with (buffer memory register) with (second near3 (processor microprocessor DSP controller microcontroller))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 10:11
S9	2	S6 and (first near3 (processor microprocessor DSP controller microcontroller)) with (pointer) with (second near3 (processor microprocessor DSP controller microcontroller))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 10:15

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S10	4908	((second near3 (processor microprocessor DSP controller microcontroller))(co?processor)) with (request$4 command$4 instruction) with (message data) with (first (processor microprocessor DSP controller microcontroller))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 10:13
S11	2327	((second near3 (processor microprocessor DSP controller microcontroller))(co?processor)) with (request$4) with (message data) with (first (processor microprocessor DSP controller microcontroller))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 10:13
S12	752	((second near3 (processor microprocessor DSP controller microcontroller))(co?processor)) with (request$4) with (send$4 transmit$4) with (message data) with (first (processor microprocessor DSP controller microcontroller))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 11:14
S13	227	((second near3 (processor microprocessor DSP controller microcontroller))(co?processor)) with (request$4) with (send$4 transmit$4) with (message) with (first (processor microprocessor DSP controller microcontroller))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 10:14
S14	3	S13 and (first near3 (processor microprocessor DSP controller microcontroller)) with (pointer) with ((second near3 (processor microprocessor DSP controller microcontroller))(co?processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 10:15
S15	465	((send$4 trasmit$4) near3 (pointer handle descriptor)) with (processor microprocessor DSP controller microcontroller co?processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 10:22
S16	8	S5 and S15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 10:23

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S17	227	((second near3 (processor microprocessor DSP controller microcontroller))(co?processor)) with (request$4) with (send$4 transmit$4) with (message) with (first (processor microprocessor DSP controller microcontroller))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 11:15
S18	30	(first (processor microprocessor DSP controller microcontroller)) with (transmit send) with (pointer) with ((second near3 (processor microprocessor DSP controller microcontroller))(co?processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 11:57
S19	379	(first (processor microprocessor DSP controller microcontroller)) with (allocat$4) with (buffer memory mailbox) with ((second near3 (processor microprocessor DSP controller microcontroller))(co?processor))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 11:58
S20	10	(first (processor microprocessor DSP controller microcontroller)) with (allocat$4) with (buffer memory mailbox) with ((second near3 (processor microprocessor DSP controller microcontroller))(co?processor)) with message	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 11:58
S21	119	(request$4) with (send$4 transmit$4 transfer$4) with (message) with (allocat$4) with (buffer descriptor memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 12:14
S22	1	S5 and S21	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 12:14
S23	2761	(return release) near3 pointer	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/03 09:35
S24	440	((first and second) near3 (processor microprocessor DSP controller microcontroller)) with (share$4 near3 (memory buffer))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 16:56

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S25	2	S23 and S24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 16:58
S26	20384	(return release) near3 (pointer handle descriptor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 16:58
S27	8	S26 and S24	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 16:58
S28	160	719/312.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 17:00
S29	13	S26 and S28	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/02 17:00
S30	211	((return release) near3 pointer) and ((first and second) near3 processor)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/03 09:35
S31	110	((return release) near3 pointer) and ((first and second) near3 processor) and ((shared common) near3 memory)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/03 16:40
S32	665	((return release) near3 pointer) and (((first and second) near3 processor) wtih ((shared common) near3 memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/03 09:36
S33	4	((return release) near3 pointer) and (((first and second) near3 processor) with ((shared common) near3 memory))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/03 09:36
S40	440	((first and second) near3 (processor microprocessor DSP controller microcontroller)) with (share$4 near3 (memory buffer))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/03 16:40

S41	22	radio and S40	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2006/03/03 16:40